EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	1793	(711/103).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/04 20:30
L4	2341	(711/202,203).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/04 20:30
L5	3672	(714/718,719).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/04 20:30
L6	1087	(711/131,149,150).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/04 20:30
L7	0	L3 AND L4 AND L5 AND L6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L8	0	L3 AND L4 AND L5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L9	3	L4 AND L5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L10	268726	"SoC" System-on-chip "system on chip" system-on-a-chip "system on a chip" "system on programmable chip" "system on a programmable chip"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30

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L11	4297	System-on-chip "system on chip" system-on-a-chip "system on a chip" "system on programmable chip" "system on a programmable chip"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L12	280413	"nonvolatile memory" ((nonvolatile non-volatile) ADJ memory) "flash memory" EPROM "electrically erasable programmable read only memory" flashchip flashdisc flashdisk flashcard flashram flashrom (flash ADJ (memory OR memories OR bios OR chip OR chips OR card OR cards OR disk OR disc OR ram OR rom OR storag$5 OR module$5)) (flash ADJ (random ADJ access ADJ memory OR read ADJ only ADJ memory)) (flash ADJ "electrically erasable programmable read only memory") "flash EPROM" FEEPROM "flash EEPROM" E2PROM EAROM EEPROM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L13	2	(NVSBI ADJ memory) "non-volatile semiconductor-based imperfect"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L14	3	(NVSBI) "non-volatile semiconductor-based imperfect"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L15	13	L5 AND L11 AND L12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L16	17	L5 AND L10 AND L12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30

EAST Search History

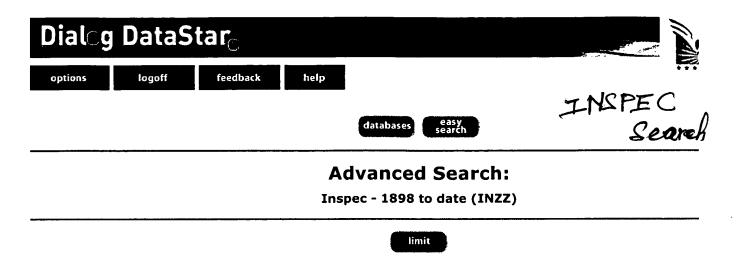| L17	685	L5 AND L12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L18	1	L17 AND L4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L19	10684	((block sector section partition) NEAR5 (address location)) WITH (translat$3 conver$4 transform$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L20	16	L17 AND L19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L21	399239	(block sector section partition) WITH (translat$3 conver$4 transform$5 map)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L22	1970	((logic logical) NEAR5 block NEAR5 address) and ((physical real) NEAR5 block NEAR5 address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L23	3	L22 AND L5 AND L12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L24	10	L22 AND L5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30

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L25	3	L17 AND L22	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L26	442	((memory) NEAR2 ((error fault) NEAR2 detect$3)) WITH (controller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L27	2044	((error fault defect) NEAR2 (detect$3 determin$5)) WITH (controller) WITH (memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L28	1970	((logic logical) NEAR5 block NEAR5 address) and ((physical real) NEAR5 block NEAR5 address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L29	58	L27 AND L28	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L33	4748	"circular buffer"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L34	2044	((error fault defect) NEAR2 (detect$3 determin$5)) WITH (controller) WITH (memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L35	1970	((logic logical) NEAR5 block NEAR5 address) and ((physical real) NEAR5 block NEAR5 address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30

L36	58	L34 AND L35	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L37	1	L33 AND L36	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L38	50	L33 AND L34	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L39	2044	((error fault defect) NEAR2 (detect$3 determin$5)) WITH (controller) WITH (memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L40	1970	((logic logical) NEAR5 block NEAR5 address) and ((physical real) NEAR5 block NEAR5 address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L41	58	L39 AND L40	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30
L42	14	synchroniz$5 AND L41	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:30

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INSPEC Search

Advanced Search:
Inspec - 1898 to date (INZZ)

limit

Search history:

No.	Database	Search term	Info added since	Results	
1	INZZ	HDL OR program$5 ADJ (chip OR processor) OR FPGA OR CPLD	unrestricted	12694	show titles
2	INZZ	SoC OR System-on-chip OR system ADJ on ADJ chip OR system-on-a-chip OR system ADJ on ADJ a ADJ chip OR system ADJ on ADJ programmable ADJ chip OR system ADJ on ADJ a ADJ programmable ADJ chip	unrestricted	1409583	show titles
3	INZZ	nonvolatile ADJ memory OR (nonvolatile OR non-volatile) ADJ memory OR flash OR EPROM OR EEPROM OR NVM	unrestricted	25846	show titles
4	INZZ	(block OR sector OR section OR partition) WITH (translat$3 OR conver$4 OR transform$5 OR map)	unrestricted	9526	show titles
5	INZZ	1 AND 2 AND 3 AND 4	unrestricted	0	-
6	INZZ	1 AND 2 AND 3	unrestricted	47	show titles
7	INZZ	error NEAR correction ADJ detection	unrestricted	117	show titles
8	INZZ	error NEAR (correction OR detection)	unrestricted	29082	show titles
9	INZZ	1 AND 2 AND 3 AND 8	unrestricted	1	show titles

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Interference Search

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	6446	((HDL (program$5 ADJ (chip processor))FPGA CPLD)).CLM.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:41
L5	854	("SoC" System-on-chip "system on chip" system-on-a-chip "system on a chip" "system on programmable chip" "system on a programmable chip").CLM.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:42
L6	29837	("nonvolatile memory" ((nonvolatile non-volatile) ADJ memory) "flash memory" EPROM "electrically erasable programmable read only memory" flashchip flashdisc flashdisk flashcard flashram flashrom (flash ADJ (memory OR memories OR bios OR chip OR chips OR card OR cards OR disk OR disc OR ram OR rom OR storag$5 OR module$5)) (flash ADJ (random ADJ access ADJ memory OR read ADJ only ADJ memory)) (flash ADJ "electrically erasable programmable read only memory") "flash EPROM" FEEPROM "flash EEPROM" E2PROM EAROM EEPROM).CLM.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:42
L7	36063	((block sector section partition) WITH (translat$3 conver$4 transform$5 map)).CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:43
L8	0	L2 AND L5 AND L6 AND L7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:43
L9	10	L2 AND L6 AND L7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/04 20:43